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	1	TALWAT et al., "Study of Thermal Processing (LTP) to Meet Sub 130 nm Node Shallow Junction Requirements", 2002, pages 175-177	<input checked="" type="checkbox"/>
	2	ITO et al., "Flash Lamp Annealing Technology for Ultra-shallow Junction Formation", Extended Abstracts of International Workshop on Junction Technology, 2002, pages 23-26	<input checked="" type="checkbox"/>
	3	KAGAWA et al., "Influence of pulse duration on KrF excimer laser annealing process for ultra shallow junction formation", Extended Abstracts of International Workshop on Junction Technology, 2002, pages 31-34	<input checked="" type="checkbox"/>
	4	Yamamoto et al., "Impact of Pre-Amorphization for the Reduction of Contact Resistance Using Laser Thermal Process, Extended Abstracts of International Workshop on Junction Technology, 2002, pages 27-30	<input checked="" type="checkbox"/>
	5	ITO et al., "Improvement of Threshold Voltage Roll-off by Ultra-shallow Junction Formed by Flash Lamp Annealing", Symposium on VLSI Technology Digest of Technical Papers, 2003	<input checked="" type="checkbox"/>

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CERTIFICATION STATEMENT

Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

☐ That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

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☐ See attached certification statement.

☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ None

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Michael W. Garvey/	Date (YYYY-MM-DD)	2006-08-04
Name/Print	Michael Garvey	Registration Number	35878

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